## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Hirosh Serial Filed: For:	pplication of: i MASUYA of Yamagata-ke No: Not assigned September 17, 2003 Semiconductor Device and Electronic Instrument		ıfactur	ing the Same, Circu	iit			2264 U.S. PTG 10/664585	
Comm P.O. B	top PATENT APPLICATION issioner for Patents ox 1450 idria, VA 22313-1450	N						οί ≣	
Dear S CORR	Sir: ESPONDENCE ADDRESS	<b>3:</b>	⊠ Custo	omer Number 000020	6021				
Transmitted herewith for filing is the patent application identified above.									
	ITEM	TOTAL NO. OF CLAIMS	CULA	NO. OF CLAIMS OVER BASE	LG/S		\$ AMOUNT	\$ FEE	
Α	TOTAL CLAIMS FE E	20	-20	0	LG=\$18 SM=\$9	\$18	\$ 0		
В	INDEPENDENT CLAIMS FEE*	2	-3	0	LG=\$84 SM=\$42	\$84	\$ 0		
С	SUBTOTAL - ADDITIONAL CLA	MS FEE (ADD FINAL COL	UMN IN	LINES A + B)				0	
D	MULTIPLE-DEPENDENT CLAIM	S FEE					TY FEE = \$280 TY FEE = \$140	\$ 0	
E	LADGE ENTITY FEE = \$750							\$ 750	
F								\$ 750	
G								\$ 40	
	*LIST INDEPENDENT CLAIMS 1 and 12.   Please charge my Deposit Account No. 50-1314 the amount of \$_0 A copy of this letter is enclosed.								
A check in the amount of \$_0_ to cover the filing fee is enclosed.  A check in the amount of \$_0_ to cover the filing fee is enclosed.  A check in the amount of \$_40.00_ to cover Assignment Recordation fee is enclosed.  The Commissioner is hereby authorized to charge any deficiency for the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-1314. A copy of this sheet is enclosed.  Any additional filing fees required under 37 C.F.R. 1.16  Any patent application processing fees under 37 C.F.R. 1.17									
Respectfully submitted HOGAN & HARTSON LIFE.									
Date:	September 17, 2003	By: Anthony J.	M()V		•	····			
		Registration Attorney for	1 No 141,2						

Attorney Docket No: 81751.0066

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re application	Art Unit:	Not assigned	
liroshi MASUYA	oshi MASUYA		Not assigned
erial No: Not	assigned		
iled: Septemb	er 17, 2003		
or: Semicon Same, C	ductor Device and Method of Manufacturing the ircuit Board, and Electronic Instrument		
iled: Septemb	er 17, 2003 ductor Device and Method of Manufacturing the		

"Express Mail" Mailing Label No. EV 325 217 457 US
Date of Deposit: September 17, 2003

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

I hereby certify that

two copies of a letter of transmittal

patent application (<u>17</u> page(s) of specification; <u>20</u> claim(s); <u>1</u> page(s) of abstract

certified copy of <u>Japanese</u> patent application No. <u>2002-281082</u> which was filed <u>September 26, 2002</u> from which priority is claimed in the subject case pursuant to 35 U.S.C. § 119

return postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service with sufficient postage under 37 C.F.R. § 1.10 on the date indicated above and are addressed to:

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Arlington, VA 22313-1450

Date: September 17, 2003

Hogan & Hartson, LLP 500 South Grand Avenue, Suite 1900 Los Angeles, California 90071

Telephone: 213-337-6700 Facsimile: 213-337-6701

Name of person making papers

Signature